Circuit Diagram for the upd7801xx as triplex LCD driver

LCD-09X3 LCD1 +5V +5V +5U COM3 COM2 COM1 꼾门~ COM2 202 **₹**∐~ 202 +5V GND GND GND で口草 C4 1000-F P56-A14
P57-A15
P60
P61
P62
P64-P61
P65-A1R
P65-A8TB
RESET
P00-10-T
P01-INTP
P03-INTP
P03-INTP
UDD
UDD
11-AN11
P11-AN11
P11-AN14
P15-AN15
P16-AN16
P16-AN16
AVBEF SEG5 SEG4 SEG3 SEG2 SEG1 SEG0 GND COM1 +5V SEG8 \Box +5V GND GND GND GND Datum / Zeit Name **NEC** 26.01.1996 18:07:20 NEC Electronics (Europe) Gmbl Technical Product Support European Application Division Rev.Änderungs-Nr. Tag zu Gerät: Zeichnungs-Nr. TPS-9601-0101 1/1 DEM0 zu Anlage: 7801× Benennung Triplex LCD C/D

Figure 4-1: Circuit Diagram